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File No.: 9-16319-251US

October 28, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Raymond Jit-Hung Sung
Filed: December 9, 2003
Serial No.: 10/730,002
Title: Clock Logic Domino Circuits For High-Speed And Energy
Efficient Microprocessor Pipelines
Examiner: James Hyonchol CHO
Group Art Unit: 2819

Mail Stop Amendment

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, VA
22313-1450 U.S.A.

AMENDMENT

Sir:

In response to the Office Action dated July 7, 2005, please amend the above-identified patent application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Amendments to the Drawings begin on page 7 of this paper.

Remarks/Arguments begin on page 8 of this paper.

Appendix A, including drawing replacement sheets and drawing annotated sheets, is attached hereto.